BSP52T1G, BSP52T3G

NPN Small-Signal Darlington Transistor

This NPN small signal Darlington transistor is designed for use in switching applications, such as print hammer, relay, solenoid and lamp drivers. The device is housed in the SOT-223 package, which is designed for medium power surface mount applications.

Features

- The SOT-223 Package can be soldered using wave or reflow. The formed leads absorb thermal stress during soldering, eliminating the possibility of damage to the die
- Available in 12 mm Tape and Reel Use BSP52T1 to Order the 7 Inch/1000 Unit Reel
- PNP Complement is BSP62T1
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_C = 25° C unless otherwise noted)

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Rating	Symbol	Max	Unit
Collector-Emitter Voltage	V _{CES}	80	V
Collector-Base Voltage	V _{CBO}	90	V
Emitter-Base Voltage	V _{EBO}	5.0	V
Collector Current	Ι _C	1.0	А
Total Power Dissipation (Note 1) @ T _A = 25°C Derate above 25°C	P _D	0.8 6.4	W mW/°C
Total Power Dissipation (Note 2) @ T _A = 25°C Derate above 25°C	P _D	1.25 10	W mW/°C
Operating and Storage Temperature Range	T _J , T _{stg}	-65 to 150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance (Note 1) Junction-to-Ambient	$R_{\theta JA}$	156	°C/W
Thermal Resistance (Note 2) Junction-to-Ambient	R_{\thetaJA}	100	°C/W
Maximum Temperature for Soldering Purposes Time in Solder Bath	ΤL	260 10	°C Sec

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Device mounted on a FR-4 glass epoxy printed circuit board using minimum recommended footprint.

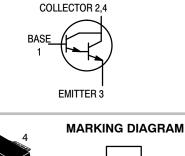
2. Device mounted on a FR-4 glass epoxy printed circuit board using 1 cm² pad.

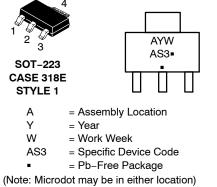


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MEDIUM POWER NPN SILICON SURFACE MOUNT DARLINGTON TRANSISTOR





ORDERING INFORMATION

Device	Package	Shipping [†]
BSP52T1G	SOT-223 (Pb-Free)	1000 / Tape & Reel
BSP52T3G	SOT-223 (Pb-Free)	4000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

BSP52T1G, BSP52T3G

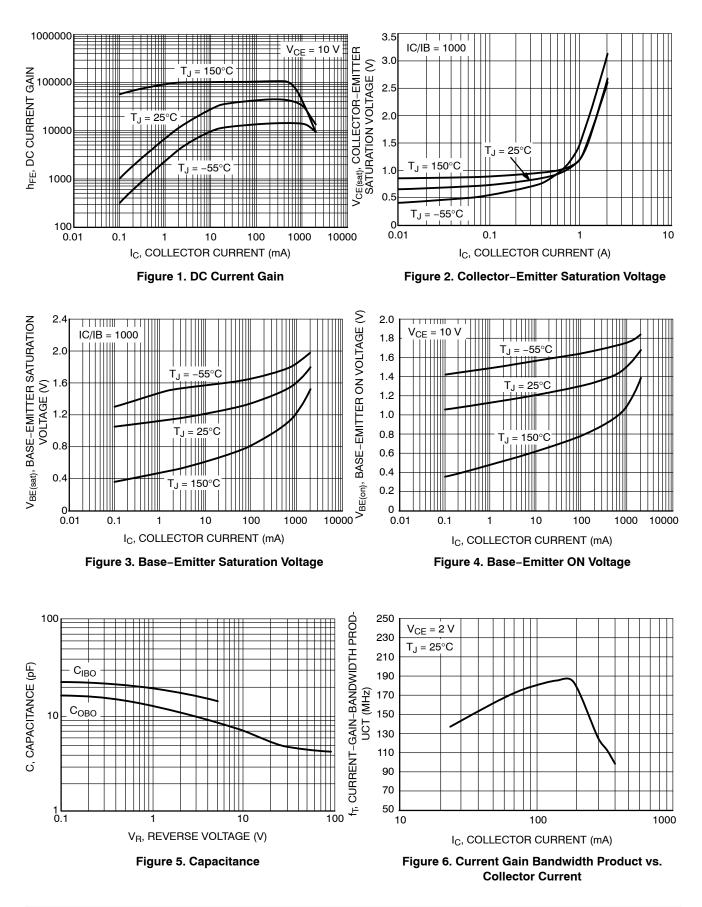
ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristics	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS			-	-	
Collector-Base Breakdown Voltage $(I_C = 100 \ \mu\text{A}, I_E = 0)$	V _{(BR)CBO}	90	_	_	V
Emitter-Base Breakdown Voltage (I _E = 10 μ A, I _C = 0)	V _{(BR)EBO}	5.0	-	_	V
Collector-Emitter Cutoff Current $(V_{CE} = 80 \text{ V}, V_{BE} = 0)$	ICES	_	-	10	μΑ
Emitter-Base Cutoff Current $(V_{EB} = 4.0 \text{ V}, I_C = 0)$	I _{EBO}	_	-	10	μΑ
ON CHARACTERISTICS (Note 3)					
DC Current Gain (I _C = 150 mA, V _{CE} = 10 V) (I _C = 500 mA, V _{CE} = 10 V)	h _{FE}	1000 2000	_		-
Collector-Emitter Saturation Voltage $(I_{C} = 500 \text{ mA}, I_{B} = 0.5 \text{ mA})$	V _{CE(sat)}	-	_	1.3	V
Base-Emitter Saturation Voltage $(I_C = 500 \text{ mA}, I_B = 0.5 \text{ mA})$	V _{BE(sat)}	-	-	1.9	V
SWITCHING CHARACTERISTICS					
Rise Time (V_{CC} = 10 V, I _C = 150 mA, I _{B1} = 0.15 mA)	t _r	_	155	-	ns
Delay Time (V _{CC} = 10 V, I _C = 150 mA, I _{B1} = 0.15 mA)	t _d	-	205	-	ns
Storage Time (V_{CC} = 10 V, I_C = 150 mA, I_{B1} = 0.15 mA, I_{B2} = 0.15 mA)	t _s	_	420	_	ns
Fall Time (V _{CC} = 10 V, I _C = 150 mA, I _{B1} = 0.15 mA, I _{B2} = 0.15 mA)	t _f	_	365	_	ns

3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%

BSP52T1G, BSP52T3G





DATE 02 OCT 2018

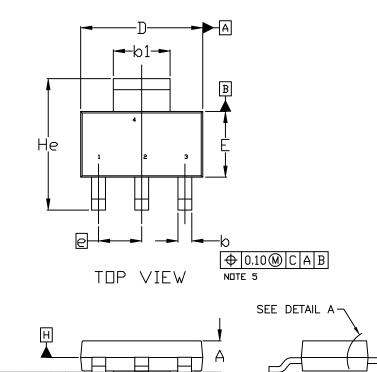




SCALE 1:1

0.10 C

A1



-11

SIDE VIEW

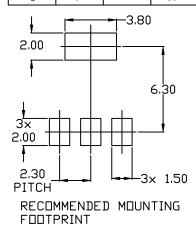
DETAIL A

NDTES:

SOT-223 (TO-261) CASE 318E-04 ISSUE R

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. DIMENSIONS D & E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.200MM PER SIDE.
- 4. DATUMS A AND B ARE DETERMINED AT DATUM H.
- 5. ALLS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING PLANE TO THE LOWEST PDINT OF THE PACKAGE BODY.
- 6. POSITIONAL TOLERANCE APPLIES TO DIMENSIONS 6 AND 61.

	MILLIMETERS			
DIM	MIN.	NDM.	MAX.	
A	1.50	1.63	1.75	
A1	0.02	0.06	0.10	
b	0.60	0.75	0.89	
b1	2.90	3.06	3.20	
с	0.24	0.29	0.35	
D	6.30	6.50	6.70	
E	3.30	3.50	3.70	
e	2.30 BSC			
L	0.20			
L1	1.50	1.75	2.00	
He	6.70	7.00	7.30	
θ	0*		10*	



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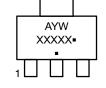
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DATE 02 OCT 2018

STYLE 1: PIN 1. BASE 2. COLLECTOR 3. EMITTER 4. COLLECTOR	STYLE 2: PIN 1. ANODE 2. CATHODE 3. NC 4. CATHODE	STYLE 3: PIN 1. GATE 2. DRAIN 3. SOURCE 4. DRAIN	STYLE 4: PIN 1. SOURCE 2. DRAIN 3. GATE 4. DRAIN	STYLE 5: PIN 1. DRAIN 2. GATE 3. SOURCE 4. GATE
STYLE 6: PIN 1. RETURN 2. INPUT 3. OUTPUT 4. INPUT	STYLE 7: PIN 1. ANODE 1 2. CATHODE 3. ANODE 2 4. CATHODE	STYLE 8: CANCELLED	Style 9: Pin 1. Input 2. Ground 3. Logic 4. Ground	STYLE 10: PIN 1. CATHODE 2. ANODE 3. GATE 4. ANODE
STYLE 11: PIN 1. MT 1 2. MT 2 3. GATE 4. MT 2	Style 12: Pin 1. Input 2. Output 3. NC 4. Output	STYLE 13: PIN 1. GATE 2. COLLECTOR 3. EMITTER 4. COLLECTOR		

GENERIC MARKING DIAGRAM*



- A = Assembly Location
- Y = Year
- W = Work Week
- XXXXX = Specific Device Code
- = Pb-Free Package
- (Note: Microdot may be in either location) *This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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