



# SGM8581

## Single-Supply, Single Rail-to-Rail I/O Precision Operational Amplifier

### PRODUCT DESCRIPTION

The SGM8581 is a single rail-to-rail input and output precision operational amplifier which has low input offset voltage, and bias current. It is guaranteed to operate from 2.5V to 5.5V single supply.

The rail-to-rail input and output swings provided by the SGM8581 make both high-side and low-side sensing easy. The combination of characteristics makes the SGM8581 good choices for temperature, position and pressure sensors, medical equipment and strain gauge amplifiers, or any other 2.5V to 5.5V application requiring precision and long term stability.

The SGM8581 is specified for the extended industrial/automotive (-40°C to +125°C) temperature range. The SGM8581 is available in the Green SOT-23-5, SOIC-8 and MSOP-8 packages.

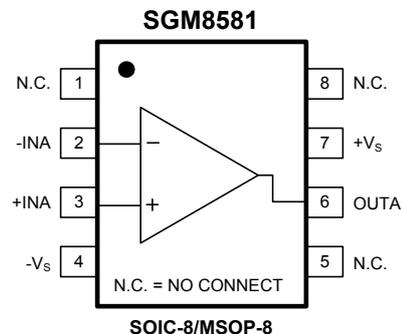
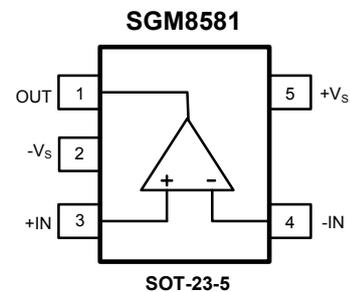
### APPLICATIONS

- Temperature Measurements
- Pressure Sensors
- Precision Current Sensing
- Electronic Scales
- Strain Gauge Amplifiers
- Medical Instrumentation
- Thermocouple Amplifiers
- Handheld Test Equipment

### FEATURES

- **Low Offset Voltage: 25 $\mu$ V (TYP)**
- **Rail-to-Rail Input and Output Swing**
- **2.5V to 5.5V Single Supply Operation**
- **Voltage Gain: 145dB (TYP) at +5V**
- **PSRR: 120dB (TYP)**
- **CMRR: 90dB (TYP)**
- **Ultra Low Input Bias Current: 15pA**
- **Low Supply Current: 445 $\mu$ A at +5V**
- **Overload Recovery Time: 70 $\mu$ s (at  $V_s = +5V$ )**
- **No External Capacitors Required**
- **-40°C to +125°C Operating Temperature Range**
- **Available in Green SOT-23-5, SOIC-8 and MSOP-8 Packages**

### PIN CONFIGURATIONS (TOP VIEW)



# SGM8581

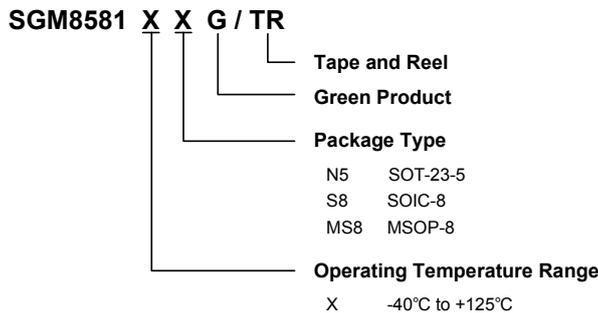
# Single-Supply, Single Rail-to-Rail I/O Precision Operational Amplifier

## PACKAGE/ORDERING INFORMATION

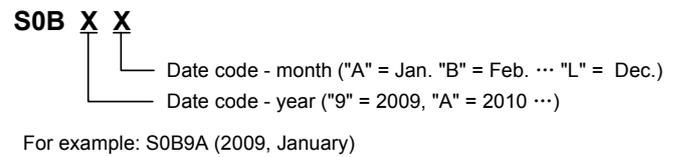
MODEL	ORDER NUMBER	PACKAGE DESCRIPTION	PACKAGE OPTION	MARKING INFORMATION
SGM8581	SGM8581XN5G/TR	SOT-23-5	Tape and Reel, 3000	S0BXX
	SGM8581XS8G/TR	SOIC-8	Tape and Reel, 2500	SGM8581XS8
	SGM8581XMS8G/TR	MSOP-8	Tape and Reel, 3000	SGM8581XMS8

NOTE: Order number and package marking are defined as the follow:

### ORDER NUMBER



### MARKING INFORMATION



## ABSOLUTE MAXIMUM RATINGS

Supply Voltage	6V
Input Voltage	-V <sub>S</sub> to (+V <sub>S</sub> ) + 0.1V
Differential Input Voltage	-5V to 5V
Storage Temperature Range	-65°C to +150°C
Junction Temperature	150°C
Operating Temperature Range	-40°C to +125°C
Lead Temperature (Soldering 10sec)	260°C
ESD Susceptibility	
HBM (SOT-23-5)	6000V
HBM (SOIC-8)	7000V
HBM (MSOP-8)	6000V
MM	400V

### NOTE:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## CAUTION

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

SGMICRO reserves the right to make any change in circuit design, specification or other related things if necessary without notice at any time. Please contact SGMICRO sales office to get the latest datasheet.

**ELECTRICAL CHARACTERISTICS**(V<sub>S</sub> = +5V, V<sub>CM</sub> = +2.5V, V<sub>O</sub> = +2.5V, T<sub>A</sub> = +25°C, unless otherwise noted.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
<b>INPUT CHARACTERISTICS</b>					
Input Offset Voltage (V <sub>OS</sub> )			25	100	μV
	-40°C ≤ T <sub>A</sub> ≤ +125°C			106	
Input Bias Current (I <sub>B</sub> )			15		pA
Input Offset Current (I <sub>OS</sub> )			10		pA
Input Voltage Range		0		5	V
Common Mode Rejection Ratio <sup>(1)</sup> (CMRR)	V <sub>CM</sub> = 0V to 5V	80	90		dB
	-40°C ≤ T <sub>A</sub> ≤ +125°C	62			
Large Signal Voltage Gain (A <sub>VO</sub> )	R <sub>L</sub> = 10kΩ, V <sub>O</sub> = 0.3V to 4.7V	95	145		dB
	-40°C ≤ T <sub>A</sub> ≤ +125°C	91			
Input Offset Voltage Drift (ΔV <sub>OS</sub> /ΔT)	-40°C ≤ T <sub>A</sub> ≤ +125°C		100		nV/°C
<b>OUTPUT CHARACTERISTICS</b>					
Output Voltage High (V <sub>OH</sub> )	R <sub>L</sub> = 100kΩ to -V <sub>S</sub>	4.99	4.998		V
	-40°C ≤ T <sub>A</sub> ≤ +125°C	4.979			
	R <sub>L</sub> = 10kΩ to -V <sub>S</sub>	4.98	4.994		V
	-40°C ≤ T <sub>A</sub> ≤ +125°C	4.96			
Output Voltage Low (V <sub>OL</sub> )	R <sub>L</sub> = 100kΩ to +V <sub>S</sub>		2	10	mV
	-40°C ≤ T <sub>A</sub> ≤ +125°C			11	
	R <sub>L</sub> = 10kΩ to +V <sub>S</sub>		6	15	mV
	-40°C ≤ T <sub>A</sub> ≤ +125°C			18	
Short Circuit Limit (I <sub>SC</sub> )	V <sub>O</sub> = 2.5V, R <sub>L</sub> = 10Ω to GND	40	45		mA
	-40°C ≤ T <sub>A</sub> ≤ +125°C	26			
<b>POWER SUPPLY</b>					
Power Supply Rejection Ratio <sup>(1)</sup> (PSRR)	V <sub>S</sub> = 2.5V to 5.5V	90	120		dB
	-40°C ≤ T <sub>A</sub> ≤ +125°C	73			
Quiescent Current (I <sub>Q</sub> )	V <sub>O</sub> = +V <sub>S</sub> /2		445	700	μA
	-40°C ≤ T <sub>A</sub> ≤ +125°C			845	
<b>DYNAMIC PERFORMANCE</b>					
Gain-Bandwidth Product (GBP)	A <sub>V</sub> = +100		1.45		MHz
Slew Rate (SR)	A <sub>V</sub> = +1, R <sub>L</sub> = 10kΩ, 2V Output Step		0.75		V/μs
Overload Recovery Time	A <sub>V</sub> = -100, R <sub>L</sub> = 10kΩ, V <sub>IN</sub> = 200mV (RET to GND)		0.07		ms
<b>NOISE PERFORMANCE</b>					
Voltage Noise (e <sub>n</sub> p-p)	0.1Hz to 10Hz		0.85		μV <sub>P-P</sub>
Voltage Noise Density (e <sub>n</sub> )	f = 1kHz		47.5		nV/√Hz

NOTE 1: PSRR and CMRR are affected by the matching between external gain-setting resistor ratios.

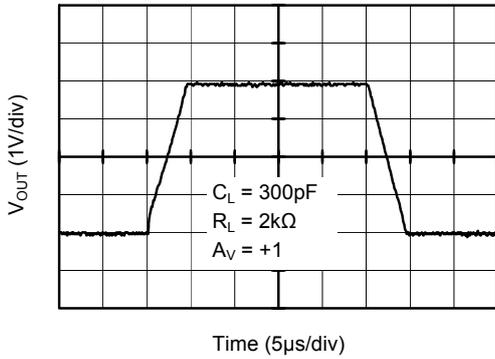
**ELECTRICAL CHARACTERISTICS**(V<sub>S</sub> = +2.5V, V<sub>CM</sub> = +1.25V, V<sub>O</sub> = +1.25V, T<sub>A</sub> = +25°C, unless otherwise noted.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
<b>INPUT CHARACTERISTICS</b>					
Input Offset Voltage (V <sub>OS</sub> )			25	100	μV
	-40°C ≤ T <sub>A</sub> ≤ +125°C			127	
Input Bias Current (I <sub>B</sub> )			15		pA
Input Offset Current (I <sub>OS</sub> )			10		pA
Input Voltage Range		0		2.5	V
Common Mode Rejection Ratio <sup>(1)</sup> (CMRR)	V <sub>CM</sub> = 0V to 2.5V	75	90		dB
	-40°C ≤ T <sub>A</sub> ≤ +125°C	61			
Large Signal Voltage Gain (A <sub>VO</sub> )	R <sub>L</sub> = 10kΩ, V <sub>O</sub> = 0.3V to 2.4V	95	140		dB
	-40°C ≤ T <sub>A</sub> ≤ +125°C	91			
Input Offset Voltage Drift (ΔV <sub>OS</sub> /ΔT)	-40°C ≤ T <sub>A</sub> ≤ +125°C		150		nV/°C
<b>OUTPUT CHARACTERISTICS</b>					
Output Voltage High (V <sub>OH</sub> )	R <sub>L</sub> = 100kΩ to -V <sub>S</sub>	2.49	2.498		V
	-40°C ≤ T <sub>A</sub> ≤ +125°C	2.473			
	R <sub>L</sub> = 10kΩ to -V <sub>S</sub>	2.48	2.497		V
	-40°C ≤ T <sub>A</sub> ≤ +125°C	2.46			
Output Voltage Low (V <sub>OL</sub> )	R <sub>L</sub> = 100kΩ to +V <sub>S</sub>		1	10	mV
	-40°C ≤ T <sub>A</sub> ≤ +125°C			11	
	R <sub>L</sub> = 10kΩ to +V <sub>S</sub>		3	15	mV
	-40°C ≤ T <sub>A</sub> ≤ +125°C			16	
Short Circuit Limit (I <sub>SC</sub> )	V <sub>O</sub> = 1.25V, R <sub>L</sub> = 10Ω to GND	20	27		mA
	-40°C ≤ T <sub>A</sub> ≤ +125°C	14			
<b>POWER SUPPLY</b>					
Power Supply Rejection Ratio <sup>(1)</sup> (PSRR)	V <sub>S</sub> = 2.5V to 5.5V	90	120		dB
	-40°C ≤ T <sub>A</sub> ≤ +125°C	73			
Quiescent Current (I <sub>Q</sub> )	V <sub>O</sub> = +V <sub>S</sub> /2		440	700	μA
	-40°C ≤ T <sub>A</sub> ≤ +125°C			786	
<b>DYNAMIC PERFORMANCE</b>					
Gain-Bandwidth Product (GBP)	A <sub>V</sub> = +100		1.45		MHz
Slew Rate (SR)	A <sub>V</sub> = +1, R <sub>L</sub> = 10kΩ, 2V Output Step		0.75		V/μs
Overload Recovery Time	A <sub>V</sub> = -100, R <sub>L</sub> = 10kΩ, V <sub>IN</sub> = 200mV (RET to GND)		0.04		ms
<b>NOISE PERFORMANCE</b>					
Voltage Noise (e <sub>n</sub> p-p)	0.1Hz to 10Hz		0.9		μV <sub>P-P</sub>
Voltage Noise Density (e <sub>n</sub> )	f = 1kHz		77		nV/√Hz

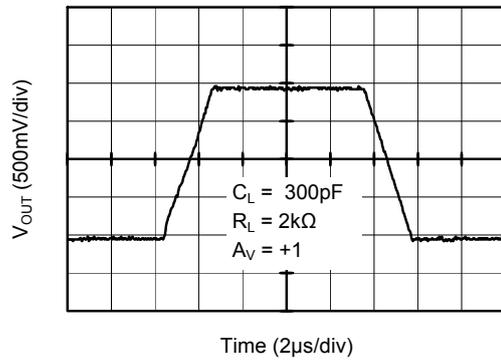
NOTE 1: PSRR and CMRR are affected by the matching between external gain-setting resistor ratios.

TYPICAL PERFORMANCE CHARACTERISTICS

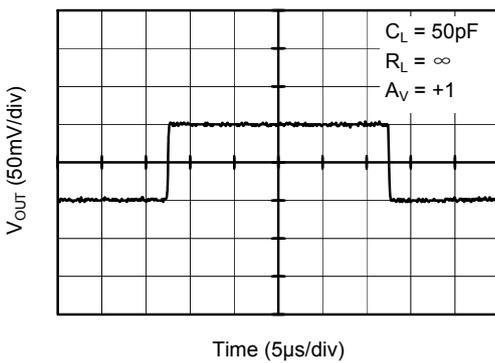
Large Signal Transient Response at +5V



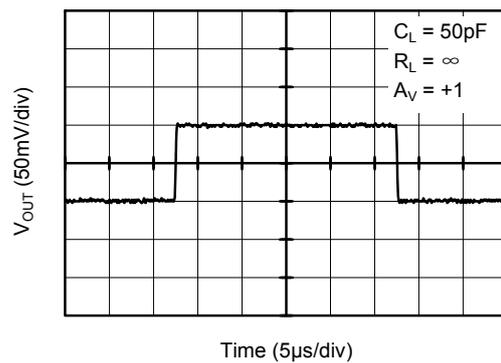
Large Signal Transient Response at +2.5V



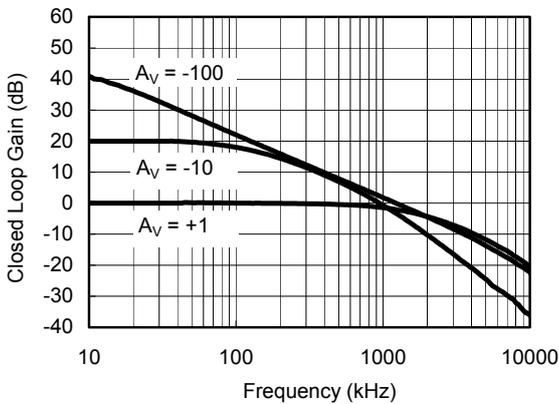
Small Signal Transient Response at +5V



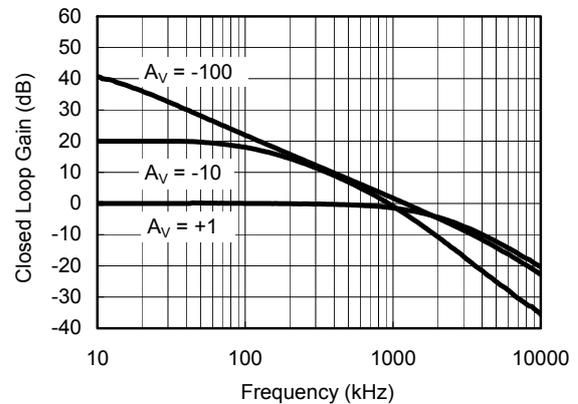
Small Signal Transient Response at +2.5V



Closed Loop Gain vs. Frequency at +5V

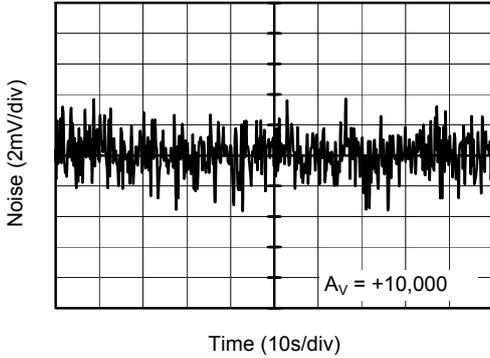


Closed Loop Gain vs. Frequency at +2.5V

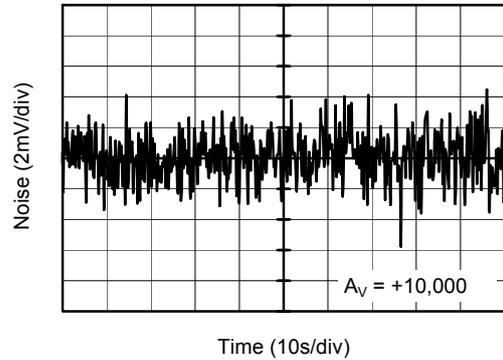


TYPICAL PERFORMANCE CHARACTERISTICS

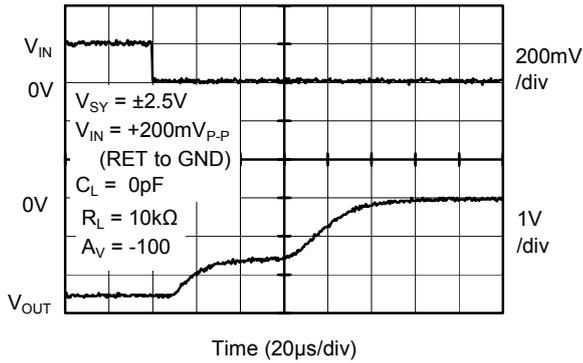
0.1Hz to 10Hz Noise at +5V



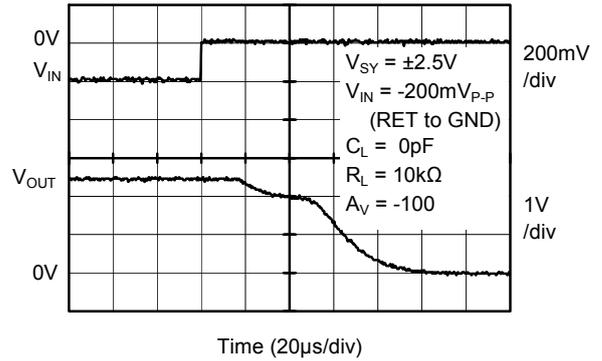
0.1Hz to 10Hz Noise at +2.5V



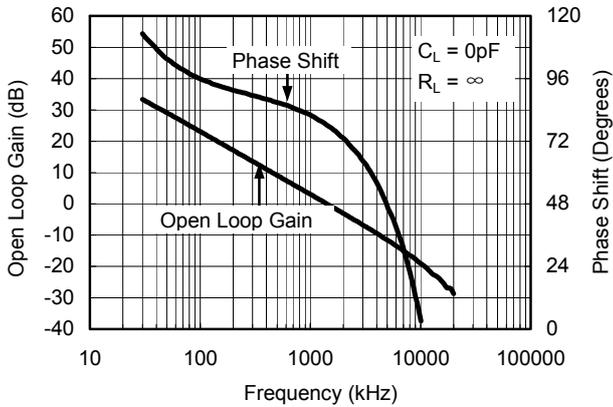
Negative Overvoltage Recovery



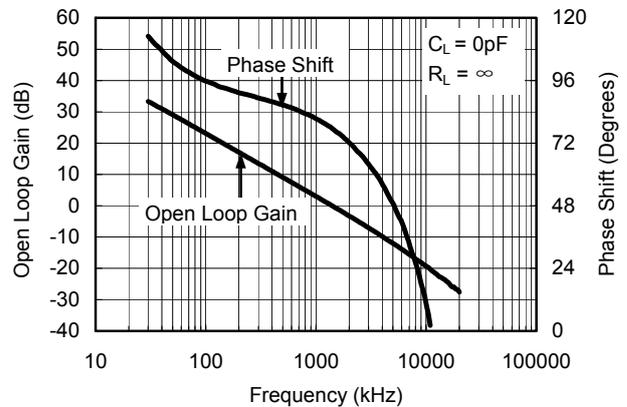
Positive Overvoltage Recovery



Open Loop Gain, Phase Shift vs. Frequency at +5V

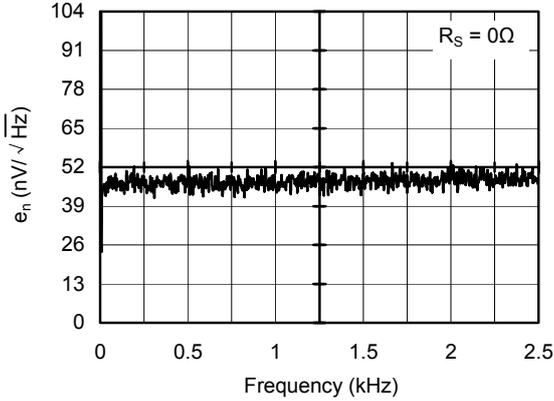


Open Loop Gain, Phase Shift vs. Frequency at +2.5V

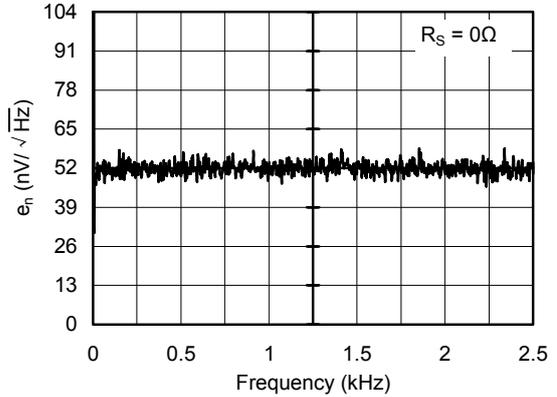


TYPICAL PERFORMANCE CHARACTERISTICS

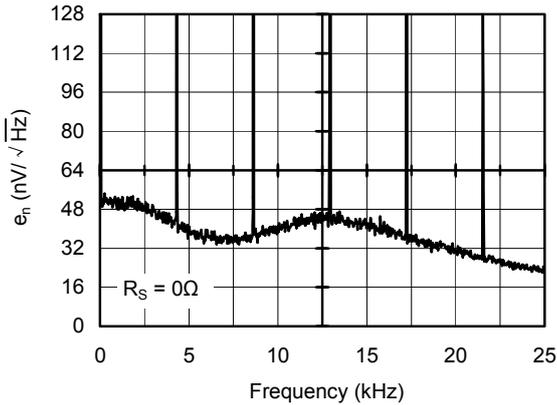
Voltage Noise Density at +5V from 0.1Hz to 2.5kHz



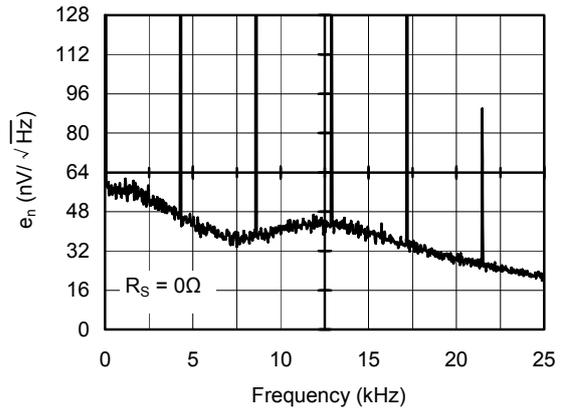
Voltage Noise Density at +2.5V from 0.1Hz to 2.5kHz



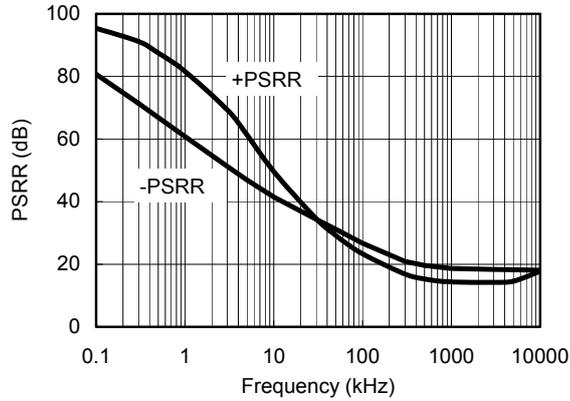
Voltage Noise Density at +5V from 0.1Hz to 25kHz



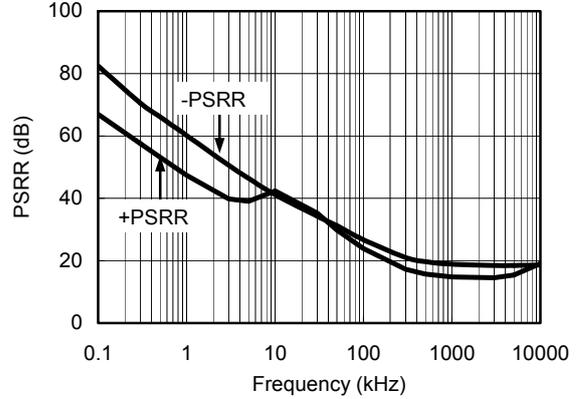
Voltage Noise Density at +2.5V from 0.1Hz to 25kHz



PSRR vs. Frequency at ±2.5V

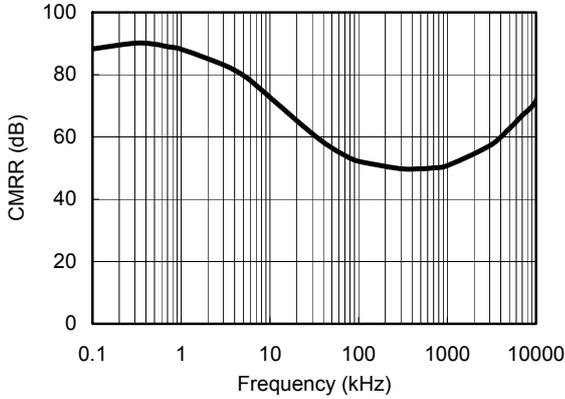


PSRR vs. Frequency at ±1.25V

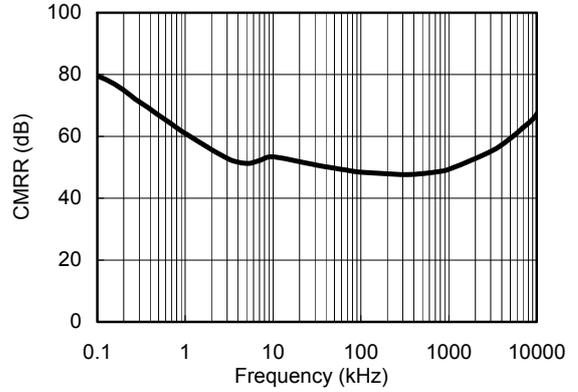


TYPICAL PERFORMANCE CHARACTERISTICS

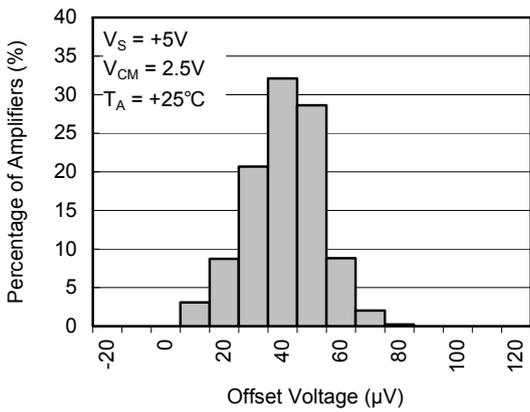
CMRR vs. Frequency at +5V



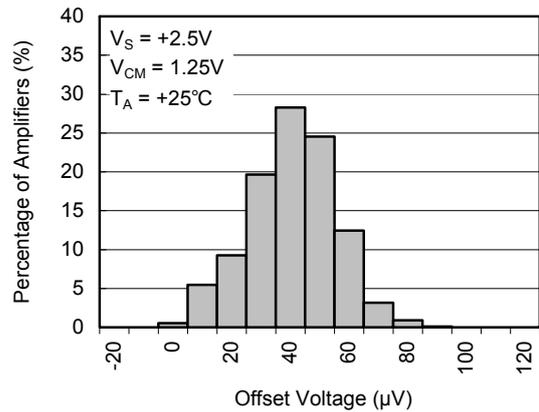
CMRR vs. Frequency at +2.5V



Offset Voltage Production Distribution at +5V

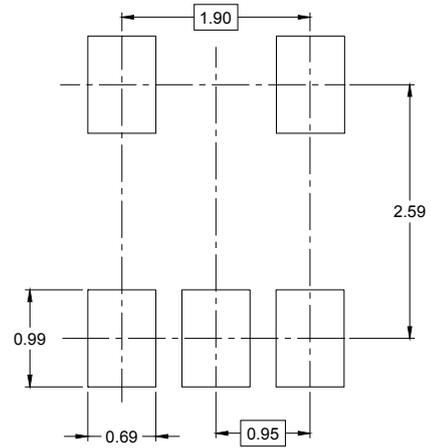
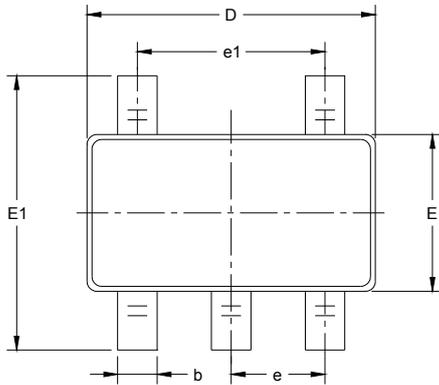


Offset Voltage Production Distribution at +2.5V

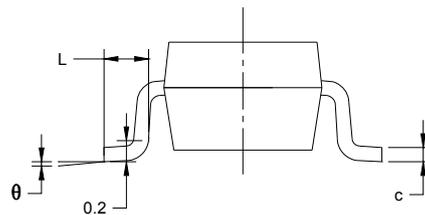
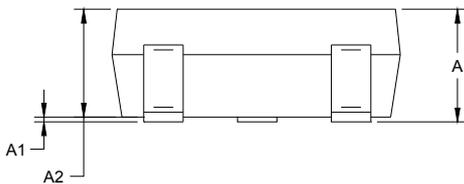


PACKAGE OUTLINE DIMENSIONS

SOT-23-5



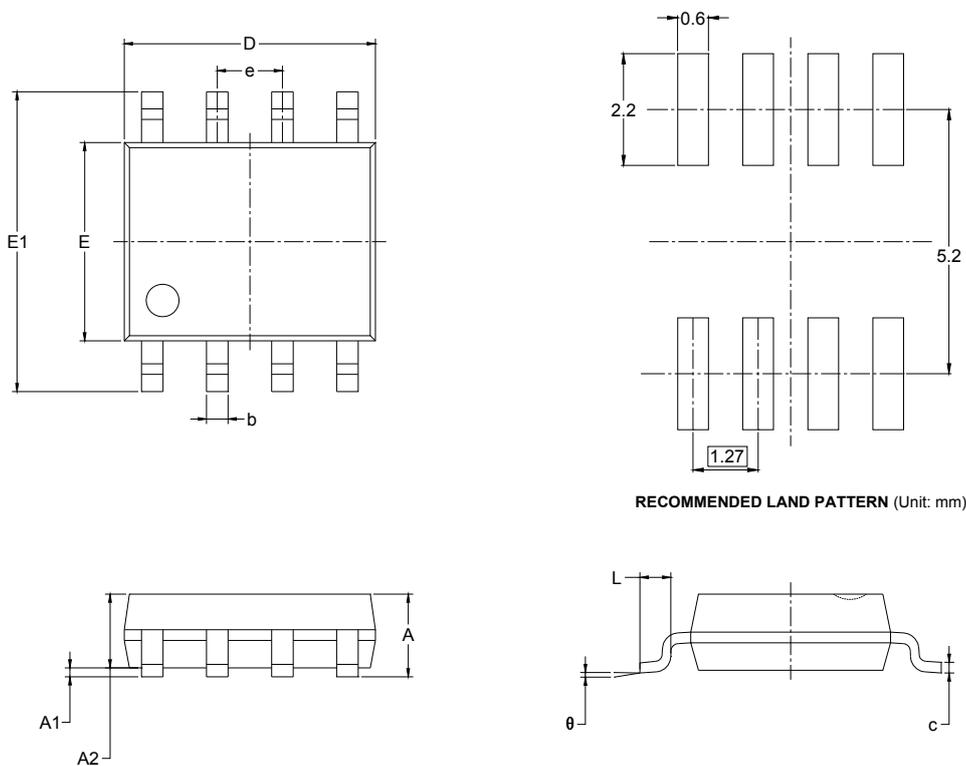
RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950 BSC		0.037 BSC	
e1	1.900 BSC		0.075 BSC	
L	0.300	0.600	0.012	0.024
theta	0°	8°	0°	8°

PACKAGE OUTLINE DIMENSIONS

SOIC-8

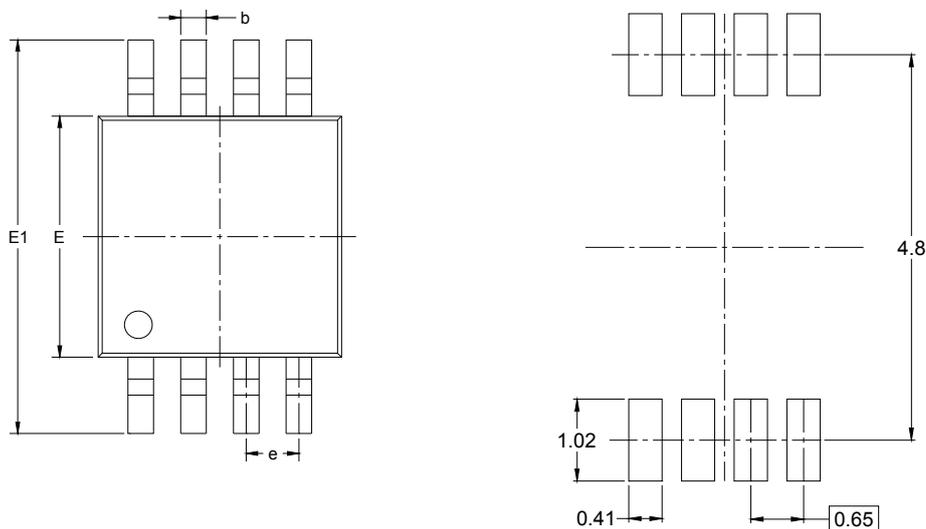


RECOMMENDED LAND PATTERN (Unit: mm)

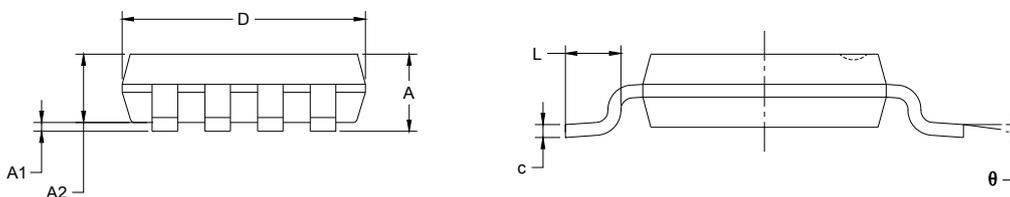
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.27 BSC		0.050 BSC	
L	0.400	1.270	0.016	0.050
$\theta$	0°	8°	0°	8°

PACKAGE OUTLINE DIMENSIONS

MSOP-8



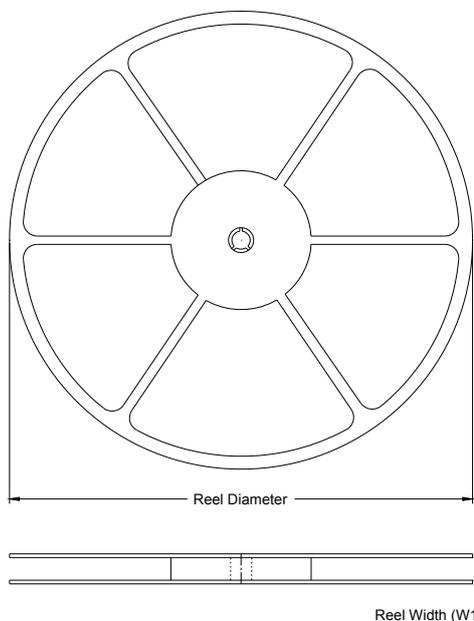
RECOMMENDED LAND PATTERN (Unit: mm)



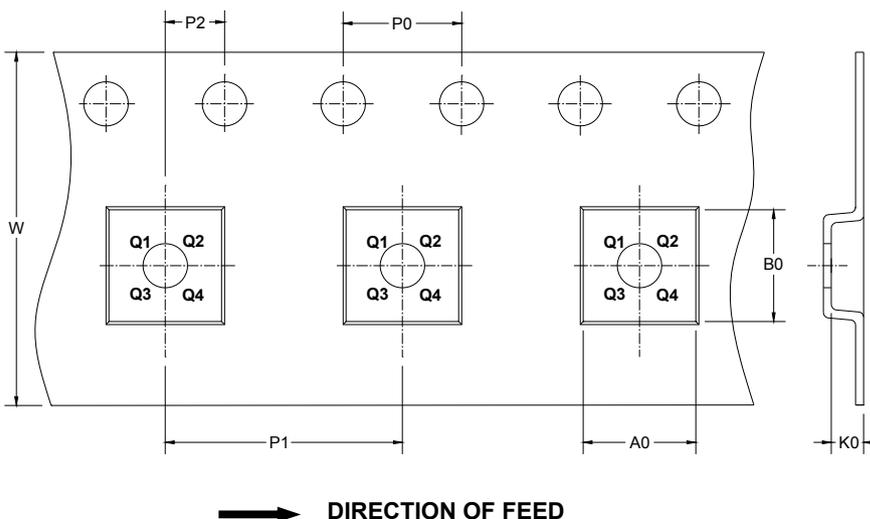
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.820	1.100	0.032	0.043
A1	0.020	0.150	0.001	0.006
A2	0.750	0.950	0.030	0.037
b	0.250	0.380	0.010	0.015
c	0.090	0.230	0.004	0.009
D	2.900	3.100	0.114	0.122
E	2.900	3.100	0.114	0.122
E1	4.750	5.050	0.187	0.199
e	0.650 BSC		0.026 BSC	
L	0.400	0.800	0.016	0.031
θ	0°	6°	0°	6°

**TAPE AND REEL INFORMATION**

**REEL DIMENSIONS**



**TAPE DIMENSIONS**

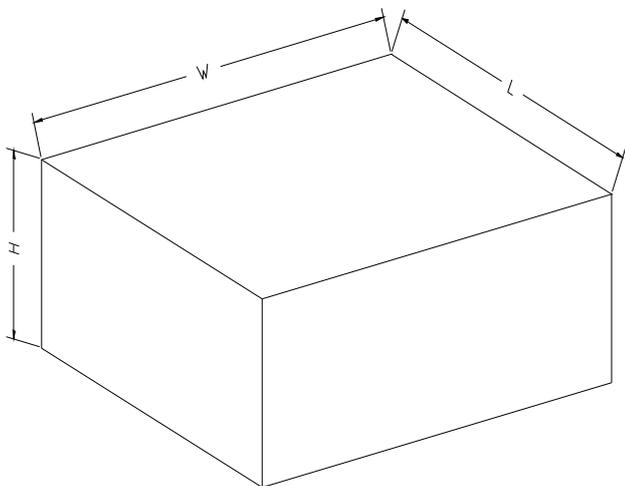


NOTE: The picture is only for reference. Please make the object as the standard.

**KEY PARAMETER LIST OF TAPE AND REEL**

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOT-23-5	7"	9.5	3.2	3.2	1.4	4.0	4.0	2.0	8.0	Q3
SOIC-8	13"	12.4	6.4	5.4	2.1	4.0	8.0	2.0	12.0	Q1
MSOP-8	13"	12.4	5.2	3.3	1.5	4.0	8.0	2.0	12.0	Q1

**CARTON BOX DIMENSIONS**



NOTE: The picture is only for reference. Please make the object as the standard.

**KEY PARAMETER LIST OF CARTON BOX**

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18
13"	386	280	370	5