



SPECIFICATIONS

CUSTOMER	:	SCHUKAT
MASS PRODUCTION CODE	:	SMMT035320240K-A10
SAMPLE VERSION	:	01
SPECIFICATIONS EDITION	:	001
DRAWING NO. (Ver.)	:	SMMT035320240K-A10-WX-A

Customer Approved

Date:

Approved	Checked	Designer
	闫伟	田冬

- ☐ Preliminary specification for design input
- ☒ Specification for sample approval





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Note : For detailed information please refer to IC data sheet :

Primacy(TFT LCD):ST7272A



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	320*240
LCD Type	Normally black, Transmissive type
Screen size(inch)	3.5 inch
Viewing Direction	FREE
Other(controller/driver IC)	ST7272A
Interface	24 BIT RGB

1.2 Mechanical Specifications

Module

Item	Standard Value	Unit
Outline Dimension	76.80(W) x 63.75 (L) x2.95(H)	mm

TFT LCD

Item	Standard Value	Unit
Active Area	70.08 (W) * 52.56 (L)	mm



1.3 Absolute Maximum Ratings

Module

Item	Symbol	Condition	Min.	Max.	Unit
Operating Temperature	T _{OP}	-	-30	80	°C
Storage Temperature	T _{ST}	-	-30	80	°C

1.4 DC Electrical Characteristics

Module

GND = 0V, Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Digital Supply Voltage	VDD	-	3.0	3.3	3.6	V
Input H/L Level Voltage	VIH	-	0.7VDD	-	VDD	V
	VIL	-	0	-	0.3VDD	V
Output H/L Level Voltage	VOH	-	VDD-0.4	-	VDD	V
	VOL	-	0	-	GND+0.4	V
Supply Current	IDD	-	-	18	30	mA



1.5 Optical Characteristics

TFT LCD Module

VDD= 3.3 V, Ta=25°C

Item		Symbol	Condition	Min.	Typ.	Max.	unit	-
Response time	Tr+Tf	25℃	-	-	25	35	ms	-
Viewing angle	Top	θY+	CR ≥ 10	70	80	-	Deg.	Note 4
	Bottom	θY-		70	80	-		
	Left	θX-		70	80	-		
	Right	θX+		70	80	-		
Contrast ratio		CR	IF=80mA	640	800	-	-	Note 3
Color of CIE Coordinate (B/L & LCD)	White	X		-0.05	0.320	+0.05	-	Note1
		Y			0.355			
	Red	X			0.605			
		Y			0.375			
	Green	X			0.340			
		Y			0.590			
	Blue	X			0.137			
		Y			0.055			
Average Brightness Pattern=white display (B/L & LCD)		IV	IF=80mA	900	1100	-	cd/m2	Note1
Uniformity (B/L & LCD)		△B	IF=80mA	75	80	-	%	Note1

Note 1:

*1: $\Delta B = B(\min) / B(\max) * 100\%$

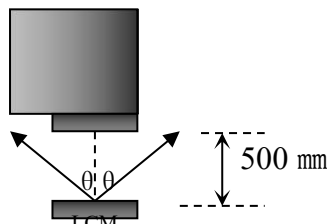
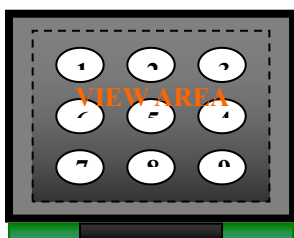
*2: Measurement Condition for Optical Characteristics:

a: Environment: 25°C±5°C / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.

b: Measurement Distance: 500 ± 50 mm , (θ= 0°)

c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.

d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%



Colorimeter=BM-7



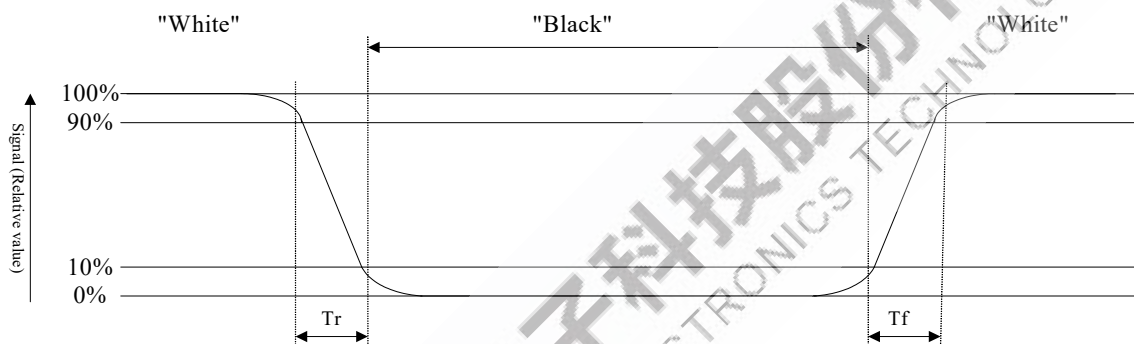
To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

Note2: Definition of response time:

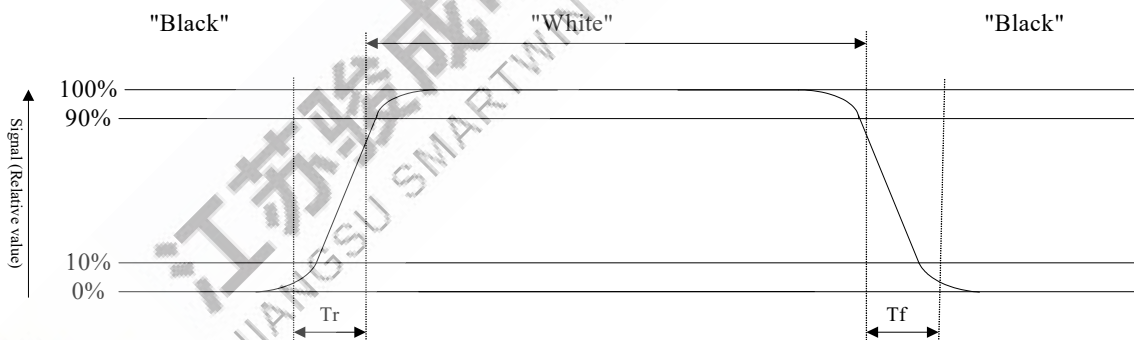
The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:

Normally White



Normally Black



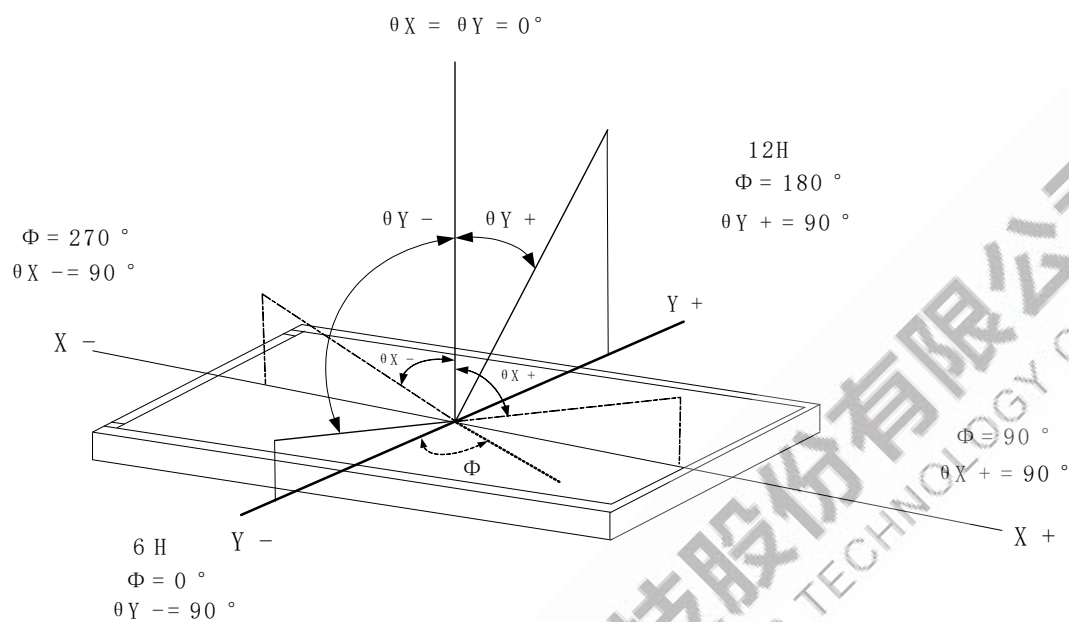
Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$$

Note4: Definition of viewing angle:

Refer to figure as below:





1.6 Backlight Characteristics

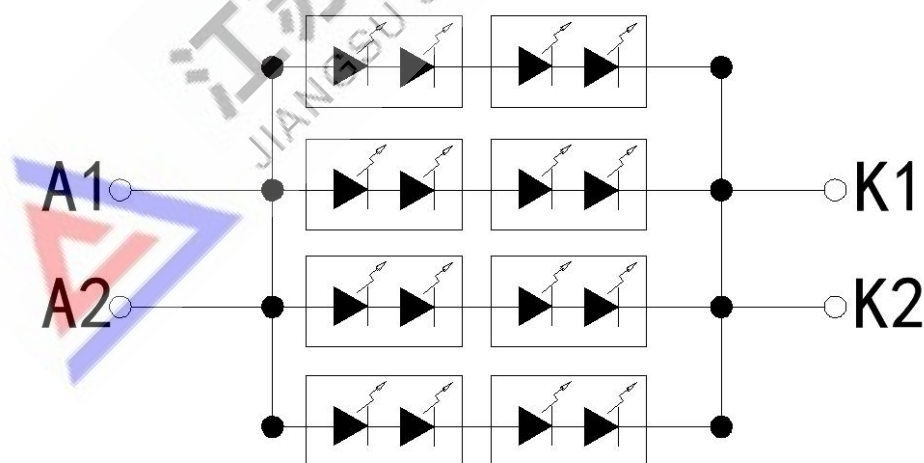
1.6.1 Backlight LED Driver IC (MP3362) Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
Led Driver Power Voltage	VLED	3.0	3.3	12	V
Led Driver Power Current	ILED	-	0.38	-	A
PWM Dimming Freq	FDIM	-	-	2	KHZ
EN threshold	EN ON	1.3	-	-	V
	EN OFF	-	-	0.4	V

1.6.2 INTERNAL Backlight Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	IF=80mA	10.8	12	13.2	V
Average Brightness	IV		18000	20000	-	cd/m ²
CIE Color Coordinate	X		-	-	-	-
	Y		-	-	-	
Led lifetime				50k		Hrs
Color		White				

Circuit Diagram





1.7 EEPROM Information

1.7.1 EEPROM Number :M24C02-RMN6TP

1.7.2 EEPROM Device Address 7bit :0X50

1.7.3 EEPROM Register Information:

Address	Description	Data	Name/Value (typ)	Notes
00h	No Data	00h	0	
01h	TFP-Interface	01h	DPI	1: DPI, 2: DSI
02h	CTP-Interface	00h	No CTP	0: No CTP, 1: I2C
03h	Manufacturer	53h	S	SM: Smartwin (ASCII)
04h		57h	M	
05h		00h		
06h	Size	03h	3.5 inch	xx.xx inch
07h		05h		
08h	X-Res.(H-Byte)	01h	320 px	
09h	X-Res.(L-Byte)	40h		
0Ah	Y-Res.(H-Byte)	00h	240px	
0Bh	Y-Res.(L-Byte)	F0h		
0Ch	Pixel-Clock(typ.)	06h	6.0 MHz	xx.xx MHz
0Dh		00h		
0Eh	Color-Depth	18h	24 bit	
0Fh	HBP(H-Byte)	00h	43	
10h	HBP(L-Byte)	2Bh		
11h	HPW	04h	4	
12h	HFP(H-Byte)	00h	8	
13h	HFP(L-Byte)	08h		
14h	VBP(H-Byte)	00h	12	
15h	VBP(L-Byte)	0Ch		
16h	VPW	04h	4	
17h	VFP(H-Byte)	00h	8	
18h	VFP(L-Byte)	08h		
19h	Polarity_Mode	24h	00100100b	Bit0: H_sync_polarity Bit1: V_sync_polarity Bit2: DE_polarity (0: Negative, 1: Positive) Bit3: H_sync_phase(Reserve) Bit4: V_sync_phase(Reserve) Bit5: DE_phase (0: Rising edge, 1: Falling edge) Bit6: Pixel_invert Bit7: DE_mode (0: DE enabled, 1: Combined Sync)
1Ah	Rotation	00h	00000000b	Bit 0: Display Mirror X Bit 1: Display Mirror Y Bit 2: Display Swap X/Y Bit 3: Reserved, 0 Bit 4: Touch Mirror X Bit 5: Touch Mirror Y Bit 6: Touch Swap X/Y Bit 7: Reserved, 0
1Bh	Reserve	00h	0	
1Ch-FFh	No Data	00h	0	



2. MODULE STRUCTURE

2.1 Interface Pin Description Interface of TFT

Pin No.	Symbol	Description
1	GND	Ground
2	GND	Ground
3	GND	Ground
4	GND	Ground
5	+3V3 BKL	3.3V Power Input for BKL
6	+3V3 BKL	3.3V Power Input for BKL
7	+3V3 BKL	3.3V Power Input for BKL
8	+3V3 BKL	3.3V Power Input for BKL
9	PWM BKL	Back light PWM Control signal
10	+3V3 TFT	3.3V Power Input for TFT
11	GND	Ground
12	R0	Red data signal
13	R1	Red data signal
14	R2	Red data signal
15	R3	Red data signal
16	R4	Red data signal
17	R5	Red data signal
18	R6	Red data signal
19	R7	Red data signal
20	G0	Green data signal



21	G1	Green data signal
22	G2	Green data signal
23	G3	Green data signal
24	G4	Green data signal
25	G5	Green data signal
26	G6	Green data signal
27	G7	Green data signal
28	B0	Blue data signal
29	B1	Blue data signal
30	B2	Blue data signal
31	B3	Blue data signal
32	B4	Blue data signal
33	B5	Blue data signal
34	B6	Blue data signal
35	B7	Blue data signal
36	GND	Ground
37	VSYNC	Vertical Sync input for TTL mode.
38	HSYNC	Horizontal Sync input for TTL mode.
39	DE	Data input enable applied to the RGB

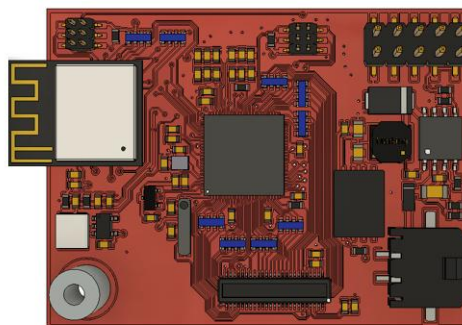


40	GND	Ground
41	PCLK	Clock signal for data latching and internal
42	GND	Ground
43	MODE(STDYB)	Standby mode, Normally pull high.
44	TFT_RESET	TFT Reset Pin
45	NC	No Connected
46	NC	No Connected
47	I2C_SDA EEPROM	EEPROM I2C Data
48	I2C_SDA EEPROM	EEPROM I2C Clock
49	3V3_EEPROM	3.3V Power Input for EEPROM
50	GND	Ground

Recommended matched connector :DF23C-50DP-0.5V

Recommended driving boards:

- ESoPe :SLD_C_W_S3
- ESoPe :SLD_C_W_P4_C6

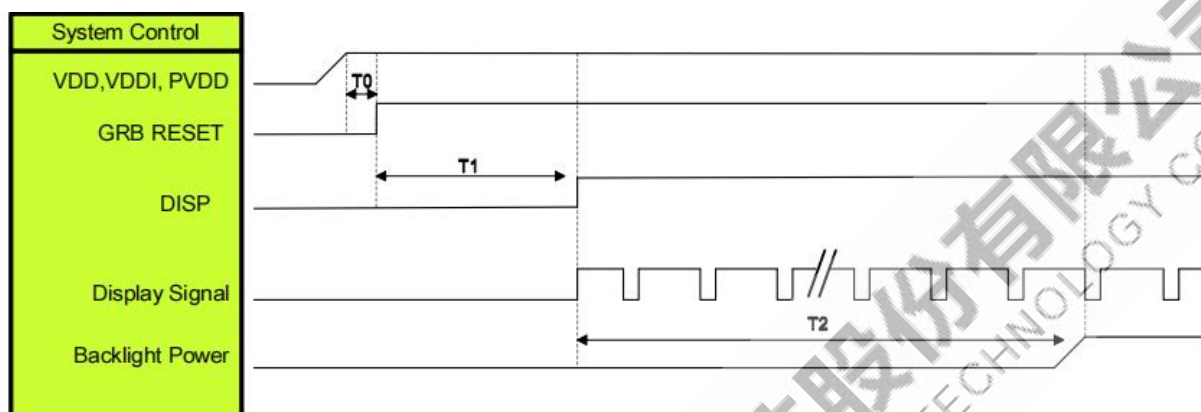




2.2 Timing Characteristics

2.2.1 Power-On/Off Timing Sequence

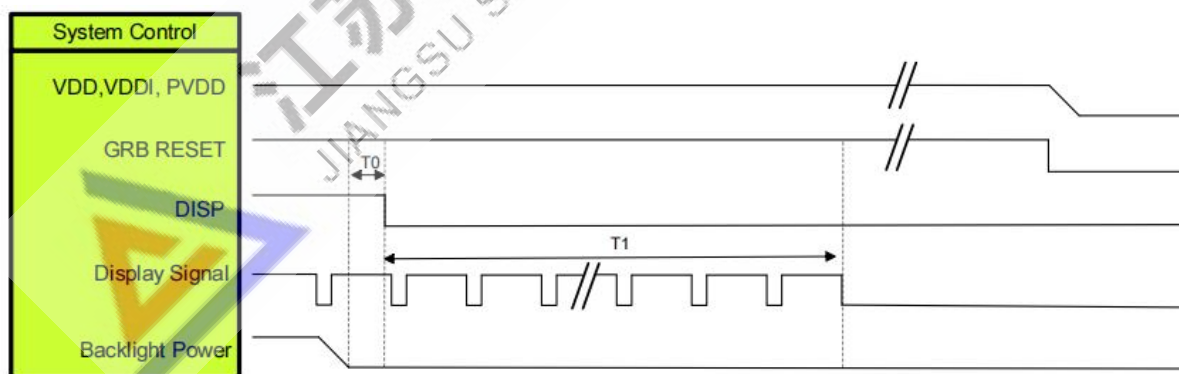
Power On Sequence



Symbol	Description	Min. Time	Unit
T0	System power stability to GRB RESET signal	0	ms
T1	GRB RESET= "High" to DISP="High"	10	ms
T2	Display Signal output to Backlight Power on	250	ms

Note: Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]

Power Off Sequence



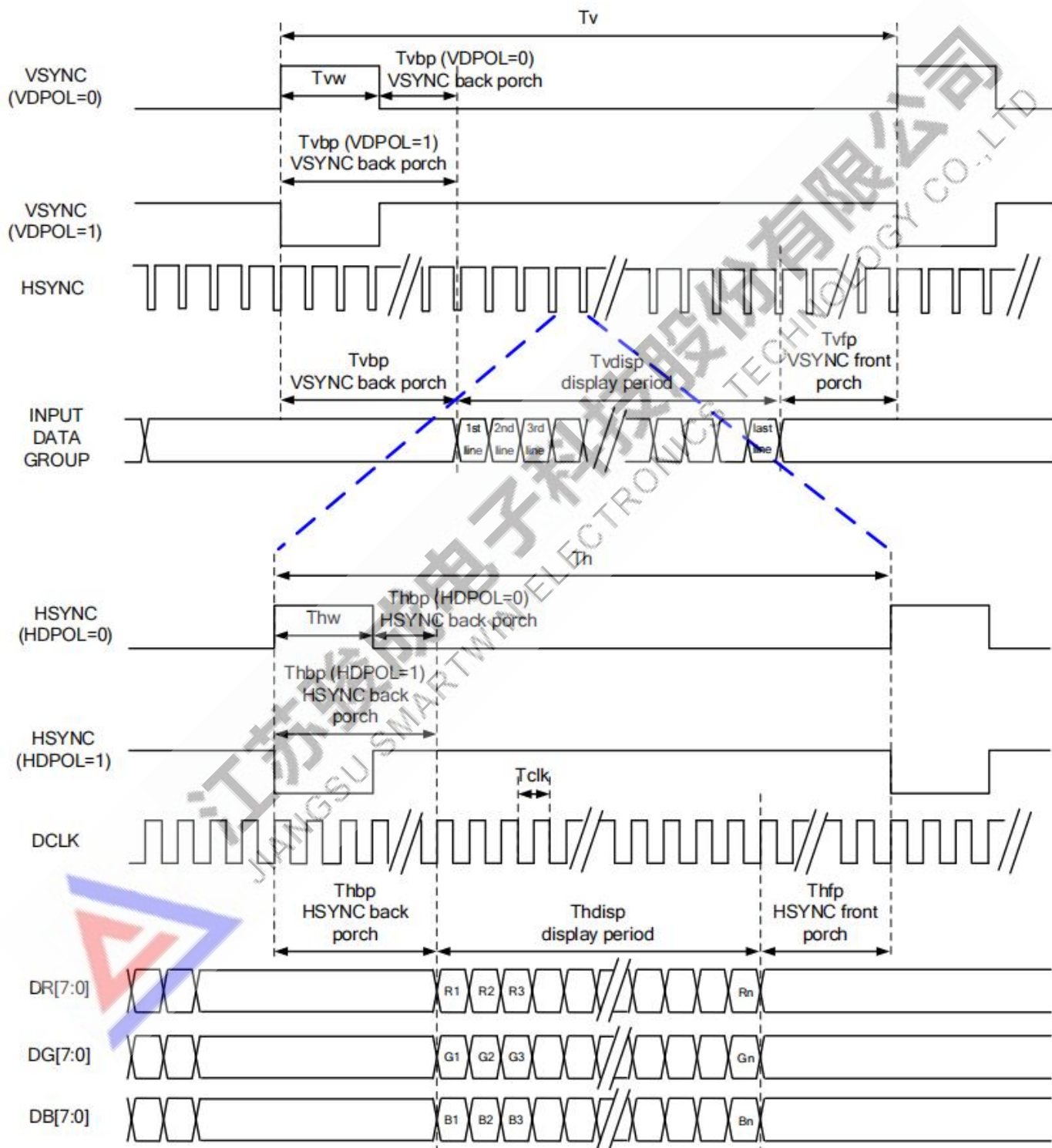
Symbol	Description	Min. Time	Unit
T0	Backlight Power off to DISP="Low"	5	ms
T1	DISP="Low" to IC internal voltage discharge complete	80	ms

Note: Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]



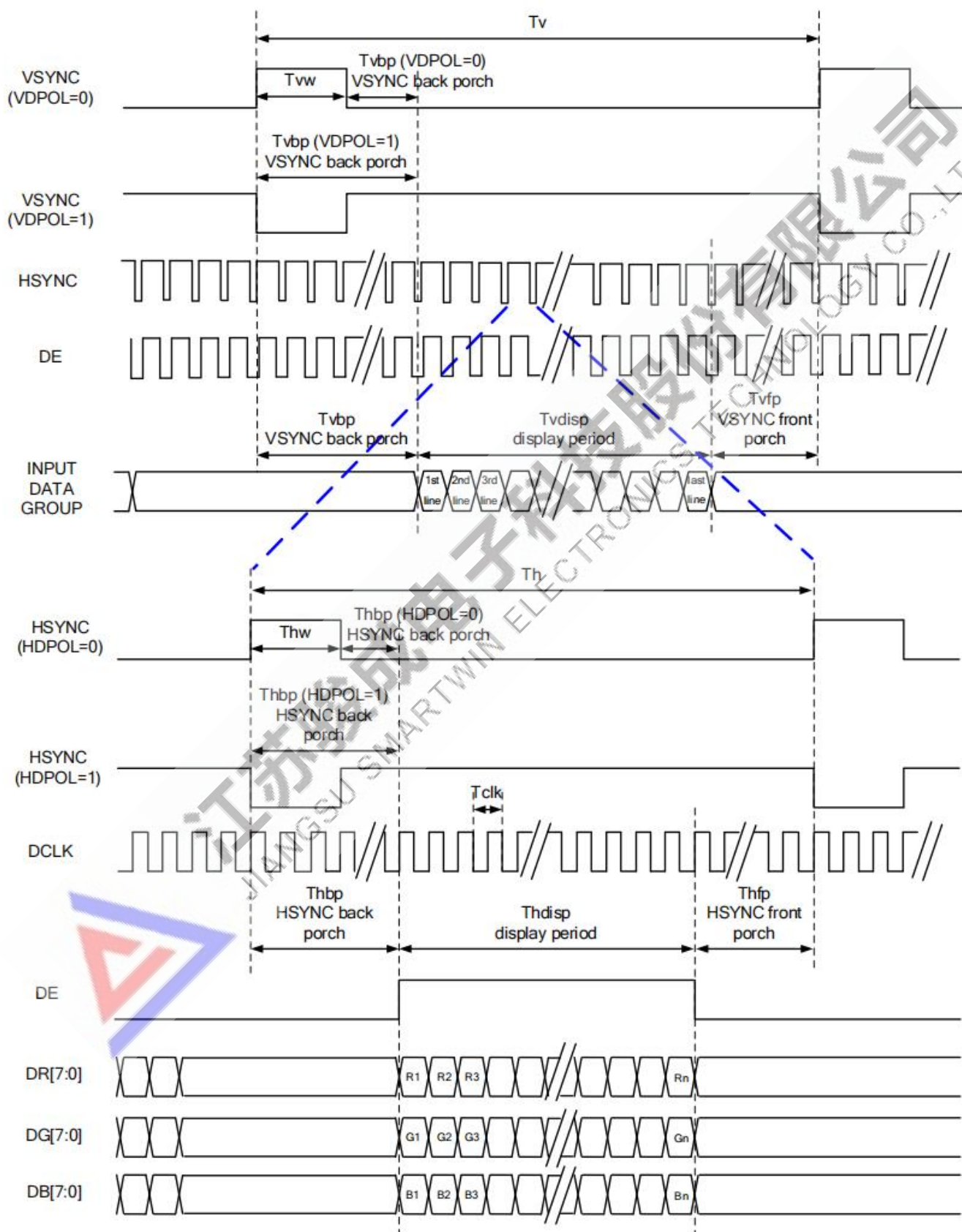
2.2.2 Parallel RGB Timing Characteristic

Parallel RGB SYNC Mode



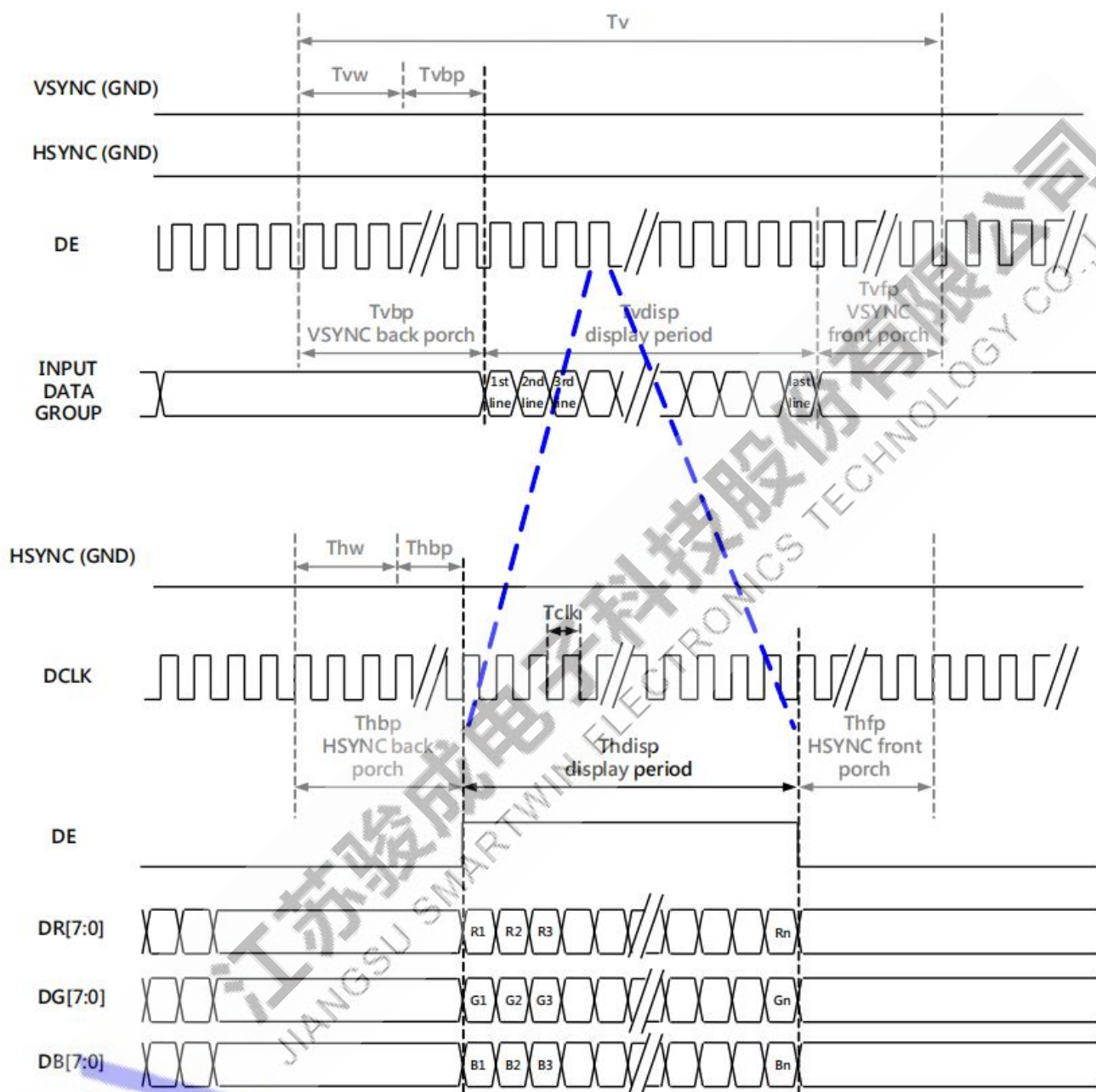


Parallel RGB SYNC-DE Mode





Parallel RGB DE Mode





Parallel RGB Input Timing Table

Parallel 24-bit RGB Input Timing (PVDD=VDD=VDDI= 3.3V, AGND= 0V, TA=25°C)

Parallel 24-bit RGB Input Timing Table						
Item	Symbol	Min.	Typ.	Max.	Unit	Note
DCLK Frequency	Fclk	5	6	8	MHz	
DCLK Period	Tclk	125	167	200	ns	
HSYNC	Period Time	Th	325	371	438	DCLK
	Display Period	Thdisp		320		DCLK
	Back Porch	Thbp	3	43	43	DCLK SYNC mode back porch control by H_BLANKING[7:0] setting Thbp= H_BLANKING[7:0]
	Front Porch	Thfp	2	8	75	DCLK
	Pulse Width	Thw	2	4	43	DCLK
VSYNC	Period Time	Tv	244	260	289	HSYNC
	Display Period	Tvdisp		240		HSYNC
	Back Porch	Tvbp	2	12	12	HSYNC SYNC mode back porch control by V_BLANKING[7:0] setting Tvbp= V_BLANKING[7:0]
	Front Porch	Tvfp	2	8	37	HSYNC
	Pulse Width	Tvw	2	4	12	HSYNC

Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.

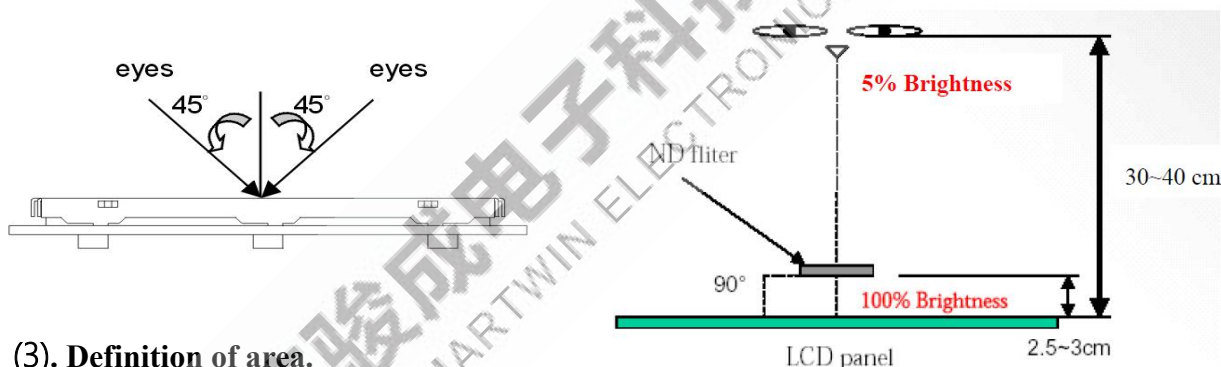


3. Inspection Specification

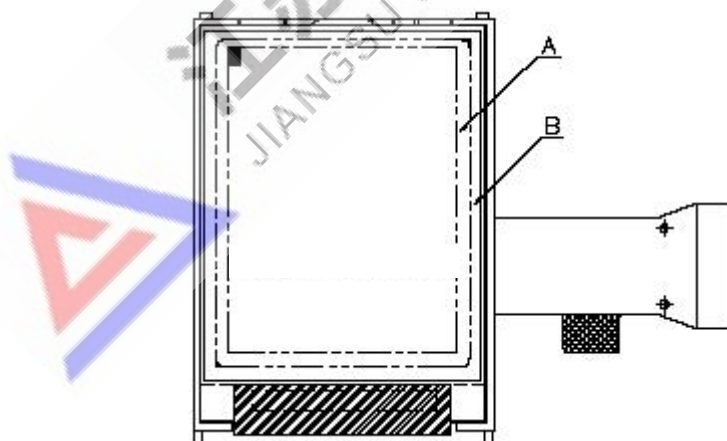
- ◆Scope: The document shall be applied to TFT-LCD Module for 0.96" ~12.3" (Ver.A01).
- ◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level II.
- ◆Equipment: Gauge、MIL-STD、SmartWin Tester、Sample
- ◆Defect Level: Major Defect AQL: 0.65 ; Minor Defect AQL: 1.5
- ◆OUT Going Defect Level: Sampling.
- ◆Standard of the product appearance test:

a. Manner of appearance test:

- (1). The test best be under 20W×2 fluorescent light(about 300lux ~500lux)
， and distance of view must be at 30~40 cm.
- (2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area : viewing area

B area : Outside of viewing area

(4). Standard of inspection: (Unit: mm)



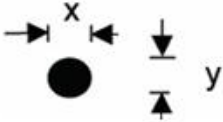
3.1 Major defect

Item No	Items to be inspected	Inspection Standard
3.1.1	All functional defects	1) No display 2) Display abnormally 3) Short circuit 4) line defect
3.1.2	Missing	Missing function component
3.1.3	Crack	Glass Crack

3.2 Minor defect

Item No	Items to be inspected	Inspection Standard	
3.2.1	Polarizer dirt and spot	For dark/white spot is defined $\varphi = (x+y)/2$ 	
		Size φ (mm)	Acceptable Quantity
		$\varphi \leq 0.15$	Ignore
		$0.15 < \varphi \leq 0.50$	2
		$0.50 < \varphi$	0



Item No	Items to be inspected	Inspection Standard	
3.2.2	Panel dirt and spot	For dark/white spot is defined $\varphi = (x+y) / 2$ 	
		Size φ (mm)	Acceptable Quantity
		$\varphi \leq 0.10$	Ignore
		$0.10 < \varphi \leq 0.25$	3
		$0.25 < \varphi$	Not allowed
3.2.3	Line Defect Including Black line White line Scratch	Define: 	
		Width(mm) Length(mm)	Acceptable Quantity
		$W \leq 0.03$	Ignore
		$0.03 < W \leq 0.07$ $L \leq 5.0$	3
		$W > 0.07$	As round type



3.2.4	Polarizer Dent/Bubble	Size ϕ (mm)		Acceptable Quantity
		$\phi \leq 0.15$		Ignore
		$0.15 < \phi \leq 0.50$		2
		$0.50 < \phi$		0
3.2.5	Electrical Dot Defect	Item		Acceptance (Q'ty)
		Dot Defect	Bright dot	≤ 4
			Dark dot	≤ 4
		Inspection pattern: full white, full black, Red, Green and blue screens. It is defined as dot defect if defect area $> 1/2$ dot. The distance between two dot defect ≥ 5 mm. Bright dot that can not be seen through 2% ND filter.		
3.2.6	Mura	Visible through ND5% at fullblack pattern is not allowed		



Note: 1. Dot defect is defined as the defective area of the dot area is larger than 50% of the dot area.

2. The distance between two bright dot defects (red, green, blue, and white) should be larger than 5mm.

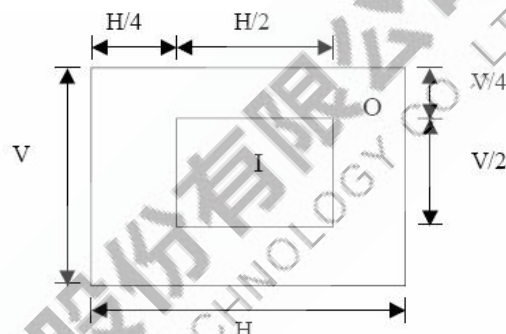
3. The distance between black dot defects or black and bright dot defects should be more than 5mm apart.

4. The definitions of the inner display area

And outer display area

I: Inner display area

O: Outer display area



5. Polarizer bubble is defined as the bubble appears on active display area. The defect of polarizer bubble shall be ignored if the polarizer bubble appears on the outside of active display area.



4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

NO.	TEST ITEM	TEST CONDITION													
1	High Temperature Storage Test	Keep in +80 ±2℃ 240 hrs Surrounding temperature, then storage at normal condition 4hrs.													
2	Low Temperature Storage Test	Keep in -30 ±2℃ 240 hrs Surrounding temperature, then storage at normal condition 4hrs.													
3	High Temperature Operating Test	Keep in +80 ±2℃ 240 hrs Surrounding temperature, then Operating at normal condition 4hrs.													
4	Low Temperature Operating Test	Keep in -30 ±2℃ 240 hrs Surrounding temperature, then Operating at normal condition 4hrs.													
5	High Temperature / High Humidity Storage Test	Keep in +60 ℃ / 90% R.H duration for 100 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)													
6	Temperature Cycling Storage Test	$-30^{\circ}\text{C} \xrightarrow{(30\text{mins})} +25^{\circ}\text{C} \xrightarrow{(5\text{mins})} +80^{\circ}\text{C} \xrightarrow{(30\text{mins})} +25^{\circ}\text{C} \xrightarrow{(5\text{mins})}$ 30 Cycle Surrounding temperature, then storage at normal condition 4hrs.													
7	ESD Test	Air Discharge: ±8kV, 5times	Contact Discharge: ±4kV, 5times												
		1. Temperature ambience : 15℃~35℃ 2. Humidity relative : 30%~60% 3. Energy Storage Capacitance(Cs+Cd) : 150pF±10% 4. Discharge Resistance(Rd) : 330 Ω±10% 5. Discharge, mode of operation : Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication : ±5%)													
8	Vibration Test (Packaged)	1. Sine wave 10 ~ 55 Hz frequency (1 min/sweep) 2. The amplitude of vibration :1.5 mm 3. Each direction (X、Y、Z) duration for 2 Hrs													
9	Drop Test (Packaged)	<table><tr><th>Packing Weight (Kg)</th><th>Drop Height (cm)</th></tr><tr><td>M < 10</td><td>100</td></tr><tr><td>10 ~ 20</td><td>80</td></tr><tr><td>20 ~ 30</td><td>70</td></tr><tr><td>30 ~ 40</td><td>60</td></tr><tr><td>40 ~ 50</td><td>50</td></tr></table> Drop Direction :※1 corner / 3 edges / 6 sides each 1time		Packing Weight (Kg)	Drop Height (cm)	M < 10	100	10 ~ 20	80	20 ~ 30	70	30 ~ 40	60	40 ~ 50	50
Packing Weight (Kg)	Drop Height (cm)														
M < 10	100														
10 ~ 20	80														
20 ~ 30	70														
30 ~ 40	60														
40 ~ 50	50														



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320\pm 10^{\circ}\text{C}$ and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .
- 5.2.10 Caution!(LCM with Capacitive Touch Panel)Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).Therefore, the touch needs to be thoroughly tested inside the target application.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

5.4 TERMS OF WARRANTY

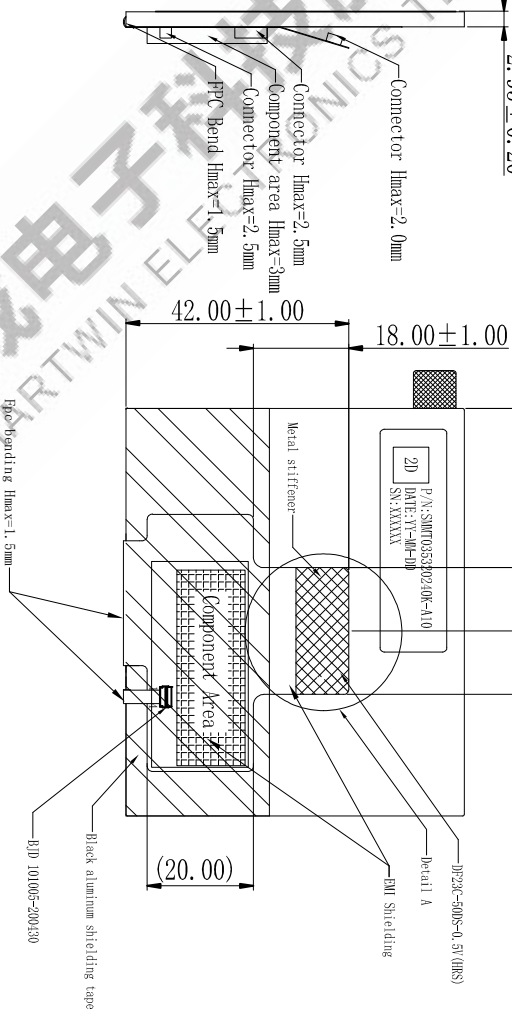
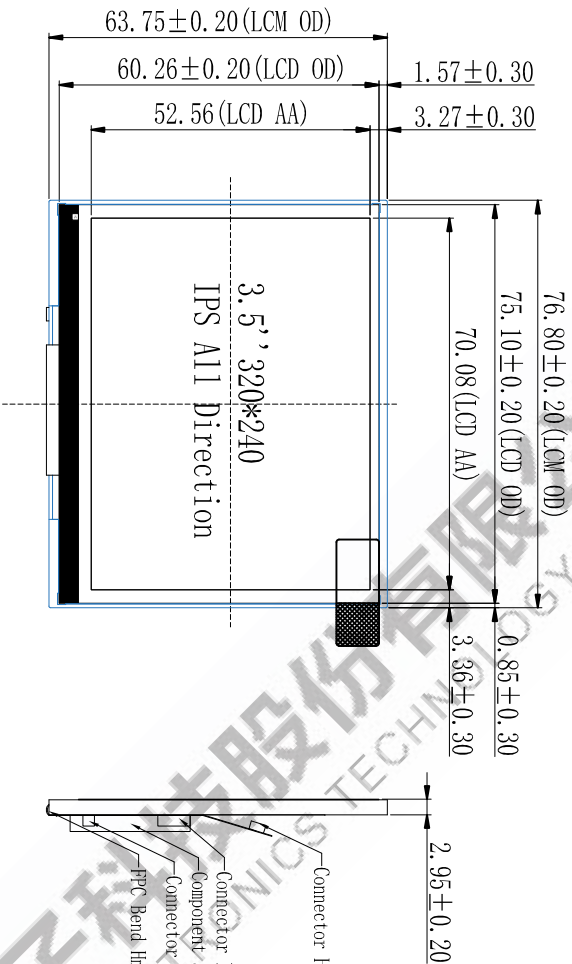
- 5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

- 5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment , we cannot take responsibility if the product is used in nuclear power control equipment , aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

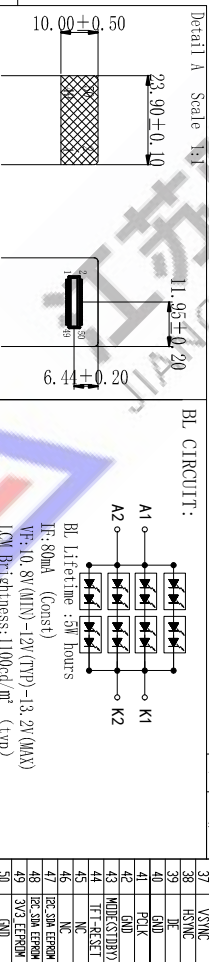
REV	REVISION RECORD	DATE
A	ACCORDING TO CUSTOM'S SPEC	2024-11-21



Note: CN1 Recommended Matched connector DP23C-50DP-0.5V (HRS)

CONFIRM ALL TYPE DRAWINGS OF THIS AND PASS BACK!

OPERATING VOLTAGE:3.3V	OPERATING CURRENT:--
DISPLAY TYPE: Normally black	VIEW DIRECTION:Free
LCD Resolution:320(H) RGB× 240(V)	LCM Interface:24 BIT RGB
LCD CONTROLLER:ST7272A-G4-1-L	CTP Interface:--
BACK LIGHT: If=80mA, Vf=1.2V (typ)	CTP CONTROLLER:--
OPERATING TEMPERATURE:-30℃ TO 80℃ (240hrs)	STORAGE TEMPERATURE:-30℃ TO 80℃ (240hrs)
DEFAULT TOLERANCE: ±0.2	ROHS STANDARD: YES



	江苏骏成电子科技有限公司 JIANGSU SMARTWIN ELECTRONICS TECHNOLOGY Co.,LTD	TITLE	模块外形图	CUSTOMER NUMBER	-	REV	A
DES' D BY	朱海	PRODUCT MODEL	SMT035320240K-A10-WX	UNIT	mm		
CHK' D BY		SERIAL NUMBER	--	DATE	2024-11-21		
APPROVED		SCALE	1:1	NO.	1/1		

PN	SYMBOL
1	A
2	A
3	A
4	K

CN1	SYMBOL
1	GN1
2	GN2
3	GN3
4	GN4
5	+3V3 BK1
6	+3V3 BK2
7	+3V3 BK3
8	+3V3 BK4
9	+3V3 BK5
10	+3V3 BK6
11	GN5
12	GN6
13	GN7
14	GN8
15	GN9
16	GN10
17	GN11
18	GN12
19	GN13
20	GN14
21	GN15
22	GN16
23	GN17
24	GN18
25	GN19
26	GN20
27	GN21
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35	GN29
36	GN30
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38	GN32
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41	GN35
42	GN36
43	GN37
44	GN38
45	GN39
46	GN40
47	GN41
48	GN42
49	GN43
50	GN44